ABSTRACT OF THE DISCLOSURE

A time sectionalized demodulator for pulse-code modulated (PCM) signal is demodulated by counting the number of carrier cycles including noise cycles to compare with a predetermined minimum number N within a time slot for a high voltage level output. When the counted number exceeds the predetermined number N, a high voltage level is outputted from the comparator. The carrier cycles including noise cycles is modulated and then sectionalized by time slots. By comparing the modulated pulse number with the preset number N within a time slot to filter out noise signal which yields pulses greater than N for high voltage level and to filter out noise signal which yields pulses less than N for low voltage level.